

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Osamu NAKAMURA

Serial No.: 10/809,118

Filed: March 25, 2004

For: Semiconductor Device And
Manufacturing Method Thereof

Examiner: Hoai V. Pham

Art Unit: 2892

Confirmation No.: 7733

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT D

Applicant has the following response to the Office Action of April 30, 2008.

Please amend the above-identified application as follows: